Hybrid Graphene-Silicon Photonics Devices

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Abstract We will review state-of-the-art of hybrid graphene silicon photonics devices, discussing electro-absorption modulators, detectors and controllable saturable absorption.

Introduction

When a light beam is normally incident on a single sheet of graphene, about 2.3% of its power is absorbed. Interestingly, this remains the case over a very broad wavelength range, from the UV up to the MIR. People have exploited this feature to demonstrate optical detectors [1] but for practical applications in the datacom or telecom domain the efficiency of such a single pass configuration is too low. A possible solution to enhance the absorption is to embed the graphene layer in a Fabry-Perot cavity. However, this introduces a strong wavelength dependence in the response of the device, negating the intrinsic broad band response of graphene. An alternative is to integrate the graphene layer on top of a waveguide. In that way the interaction length can be increased at will. As an example, when integrated on a 220 nm x 600 nm silicon waveguide, about 0.1 dB of the light (@ 1550nm) is absorbed per micrometer for light propagating in the fundamental quasi TM-mode. In addition, when applying a voltage between the graphene layer and the silicon waveguide (Fig. 1a) the Fermi level and hence the absorption in the graphene layer can be controlled. This was demonstrated first in [2], where a graphene based intensity modulator with a bandwidth of 1 GHz and extinction ratio of ~0.08 dB/ μ m, operating over the wavelength range 1.35-1.60 µm was shown. In a follow up paper [3], the authors showed a variant of this device whereby also the bottom electrode is formed by a graphene layer (Fig. 1b). This roughly doubles the extinction ratio, without increasing the capacitance of the device. An additional advantage of this approach is that the waveguide no longer serves as the bottom electrode and in principle can be realised from any dielectric material, e.g. Silicon Nitride.

Since these first demonstrations several other groups have demonstrated graphene based waveguide modulators [4-9] and detectors [12-

15]. Also several theoretical studies were made [10-11]. The biggest trade-off to make lies in the selection of the gate-oxide thickness. Decreasing the drive voltage, as required for combining the modulators with advanced CMOS drivers, requires reducing the gate oxide thickness. However. this increases the capacitance of the device and hence decreases its bandwidth. One approach to overcome this trade-off is to enhance the optical confinement in the graphene layer(s). This can be achieved by embedding a short modulator section in a planar resonator, e.g. a ring resonator [6][7]. However, this again introduces strong wavelength dependence in the response. Alternatives that do not introduce this wavelength dependence are to embed the graphene layer in the centre of a waveguide (Fig. 1c, [10]) or to combine it with a metal contact, which supports a plasmon-like mode (Fig. 1d, [11]). Such configurations have not yet been shown experimentally however.





EA-modulator

Earlier demonstrations of hybrid graphenesilicon modulators were relying on nonintentionally doped or lightly doped silicon waveguides (Fig. 1a), increasing the resistance of the bottom electrode and hence limiting the RC-bandwidth of the device. We recently demonstrated an improved version of the device, relying on imec's standard iSIPP25G platform, whereby the waveguide and the silicon contact area were intentionally doped (Fig. 2). This considerably improved the performance of the device, allowing us to demonstrate open eye diagrams for data-rates up tot 10 GBit/s.



Fig. 2 Fabricated EA-modulator (from [9])

Fig. 3 shows the fabrication scheme. The silicon waveguide is planarized using SiO_2 before transferring the graphene layer.



Fig. 3 Fabrication scheme for hybrid graphene-silicon modulators

Fig. 4 shows the eye diagrams for a 50 μ m long device, operated at speeds up to 10 GB/s, demonstrating that this device can indeed operate at data rates relevant for optical communication purposes. Further improvement (lower drive voltage, higher extinction ratio) is expected through improving the quality of the graphene layer.



Fig. 4 Optical eye diagrams measured at 1560nm for a 50μ m modulator at 6Gb/s, 8Gb/s and 10Gb/s modulation speed, using a drive voltage of 2.5Vpp swing and 1.75V forward bias delivered with a 50 terminated probe (from [9]).

Saturable Absorption

Graphene has been used extensively as the saturable absorber in modelocked lasers,

allowing to generate ultrashort pulses [16]. In [17] an all fiber based modulator was demonstrated for that purpose. Recently, we demonstrated that also the planar integrated device shown in Fig. 2 can be used to control the saturable absorption in graphene [18]. Preliminary results are shown in Fig. 5. This opens routes towards fully integrated graphene mode locked devices with controllable pulse shape.



Fig. 5 Control of saturable absorption in hybrid silicon graphene modulator (see [18] for details). At high input powers the transmission is reduced through two photon absorption in the silicon waveguide.

Conclusions

Hybrid silicon-graphene devices are rapidly gaining maturity. Their ultra wide band operation and potentially low cost processing are very promising for applications in optical telecom and datacom.

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