

Demonstration of optical via and low-loss optical crossing for vertical integration of silicon photonic circuit

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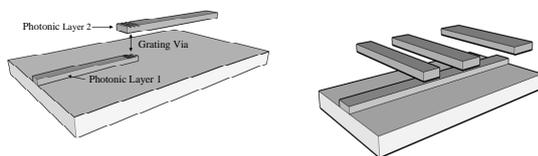
Abstract: We demonstrate optical via and ultralow-loss optical crossing in dual layer silicon photonic circuit. We have achieved crossing loss as low as 0.013 ± 0.0024 dB/crossing. By employing shallow etched grating couplers we couple light from one photonic layer to other with a coupling efficiency of 11%.

1. INTRODUCTION

With ever increasing complexity and density of photonic integrated circuits, the need for multilayer functionality is arising for next generation photonic circuitry. The vertically integrated circuit layers must be sufficiently isolated, at the same time, we should be able to transport optical signal from one layer to the other. In electronic IC's vertical integration can be easily realizable through connecting different layers through a low resistant metal line. However, such routing in optical integrated circuits is not straightforward [1].

In this paper, we demonstrate a CMOS compatible 1D grating assisted optical via and ultralow-loss waveguide crossing in a dual layer silicon photonic circuit.

2. DUAL LAYER CIRCUIT



(a) Schematic of (a) waveguide crossing and (b) optical via in a dual layer circuit

We present two types of dual layer structure: Optical Via (Fig. 1a) and waveguide crossings (Fig. 1b). The devices were fabricated using layered CMOS approach. First photonic layer was fabricated on a crystalline SOI wafer [2] and second on low-loss (3.5dB/cm) amorphous Si [3]. The two photonic layers were isolated by 940nm of silicon dioxide. Fig. 2 depicts the cross-section image of a fabricated grating via. The process temperatures were kept below 400°C to make the process CMOS back-end compatible for above-IC CMOS-Photonics integration.

The fabricated devices were optically characterized through transmission measurement. As a proof of concept, with a simple design for a grating assisted optical via, we have achieved a coupling efficiency of 11%. In case of optical crossing a loss of only 0.013dB is achieved per crossing.

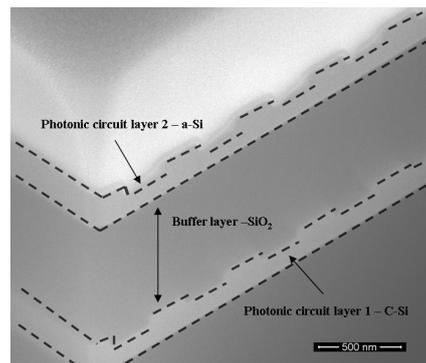


Fig.2 SEM Cross-section view showing the grating coupler in the bottom and top photonic circuit.

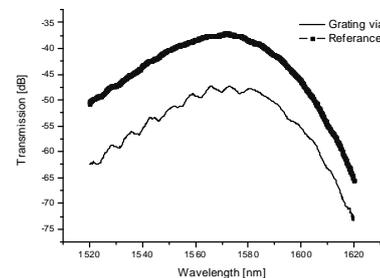


Fig. 3 Transmission spectrum of a grating optical via

3. SUMMARY

Though this paper we demonstrate an optical via with a coupling efficiency of 11% and a loss of 0.013dB/crossing for waveguide crossing in a dual layer silicon photonic circuit.

4. REFERENCE

- [1] M. Raburn, B. Liu, K. Rauscher, Y. Okuno, N. Dagli, and J. E. Bowers, "3-D photonic circuit technology," IEEE J.sel. Topics Quantum Electron., 8, 935(2002).
- [2] W. Bogaerts, R. Baets, P. Dumon, V. Wiaux, S. Beckx, D. Taillaert, B. Luyssaert, J. Van Campenhout, P. Bienstman, and D. Van Thourhout, "Nanophotonic waveguides in silicon-on-insulator fabricated with CMOS technology," J. Lightwave Technol., 23, 401(2005).
- [3] S. K. Selvaraja, E. Sleeckx, W. Bogaerts, M. Schaeckers, P. Dumon, D. Van Thourhout, and R. Baets, "Low loss amorphous silicon photonic wire and ring resonator fabricated by CMOS process," in ECOC, Germany, 2007.