Trimming of silicon ring resonator by electron beam induced compaction and strain

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Abstract: Silicon is becoming the preferable platform for future integrated components, mostly due to the mature and reliable fabrication capabilities of electronics industry. Nevertheless, even the most advanced fabrication technologies suffer from non-uniformity on wafer scale and on chip scale, causing variations in the critical dimensions of fabricated components. This is an important issue since photonic circuits, and especially cavities such as ring resonators, are extremely sensitive to these variations. In this paper we present a way to circumvent these problems by trimming using electron beam induced compaction of oxide in silicon on insulator. Volume compaction of the oxide cladding causes both changes in the refractive index and creates strain in the silicon lattice. We demonstrate a resonance wavelength red shift 4.91 nm in a silicon ring resonator.

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References and links
1. Introduction

Silicon-on-insulator (SOI) is gaining interest as preferable material system for future ultra-compact integrated photonic components. The main advantages of this material system are firstly the high refractive index contrast between silicon (core) and oxide or air (cladding) enabling small bend radii and dense integration, and secondly mature fabrication facilities thanks to the electronics industry. In spite of the indirect band gap of silicon, both passive [1-3] and active [4-7] devices have been demonstrated thanks to heterogeneous integration of III-V semiconductors. One of the applications aimed for by the telecom industry is optical filters for wavelength (de)multiplexing. SOI is the ideal platform to make these filters compact and low-cost. Several geometries, such as ring resonators [8] and photonic band gap materials [9] have good filtering characteristics. However, most of the demonstrated filters were fabricated with electron beam lithography, which is a serial fabrication technique and therefore unattractive for mass fabrication. In previous work we have demonstrated that these filters can also be fabricated in a parallel way, with 248 nm Deep-UV lithography (DUV) in a standard Complementary Metal Oxide Semiconductor (CMOS) facility [10]. However, variations of the critical dimensions of devices fabricated by optical lithography are inevitable. These variations can be caused by wafer non-uniformity, by e.g. varying layer thicknesses on wafer edges, or by non-uniformity within one chip, mainly caused by lithography imperfections near mask edges. A way to assess critical dimension variations in a photonic circuit is to evaluate the resonance wavelength shift of identically designed ring resonators, dispersed over a wafer. In practice the resonance wavelength shifts exceed 1 nm, which is unacceptable for many applications. The most common solution for this is active thermal tuning [11, 12], however, when many resonators have to be integrated on a single chip, this would lead to high power consumption and important device complexity. Another approach to circumvent these process variations is trimming of the devices after fabrication. In this paper we present a technique to locally and independently trim the resonances of ring resonators on a silicon chip. This allows for complete compensation of resonance wavelength variations on silicon photonic integrated components.
2. Electron beam induced compaction of the SiO₂ cladding

The resonance wavelength of a ring resonator is trimmed by changing the optical path length of the resonator, in our case by varying the effective index of the guided mode and not the length of the ring. An increase in effective index causes a red shift of the resonance wavelength. This was demonstrated in several (low to medium index contrast) material systems such as silica glass [13] and SiN/SiON [14, 15]. These core materials are sensitive to either UV or electron beam compaction (also other higher energy particles tend to compact these materials, but these are less commercially practicable). This means that the compacted material contains most of the optical mode and thus relatively large shifts in effective indices can be obtained. In SiN more than 10 nm of resonance wavelength shift was obtained for ring resonators operating at 1550 nm [14]. Electron beam [16-18] as well as UV [19-21] irradiation have been used to fabricate or alter waveguides in silica glass. In SOI, the preferable material system for future industrial deployment, the core material is silicon, which can not be compacted by either UV or electrons. Only the SiO₂ cladding is susceptible to compaction. Literature reports on SiO₂ volume compaction and refractive index changes upon irradiation with highly energetic particles. The particles include ions and gamma photons, by which the oxide compacts due to knock-on atom displacements, or electrons and UV photons, where the compaction is caused by ionization-induced relaxation of inherently strained Si-O bonds in the glass network [22-25]. Due to the imaging capabilities and the ease to precisely control the irradiation dose and energy we have used an electron beam (from an FEI Nova 600 scanning ion/electron microscope) to compact the SiO₂ cladding layer. The experiments presented here differ from previous reports in the fact that the optical core material – silicon – can not be compacted, thus strain induced by cladding compaction is used as trimming mechanism.

![Fig. 1. Overview of the experiment: the right ring is trimmed by electron beam compaction; the left one is kept original as a reference to exclude temperature or ambient variations.](image)

3. Experiment: 4.91 nm red shift of a silicon ring resonator

The resonance frequency of silicon ring resonators is extremely sensitive to changes of temperature and of the surrounding medium; rings are therefore attractive as sensors [26, 27]. However, in this experiment we want to exclude all external factors and investigate the resonance shifts caused only by electron beam irradiation. Therefore we have fabricated a sample with two rings, with different resonance wavelengths, serially connected to the same waveguide, as depicted in Fig. 1. Only one of these rings is irradiated by imaging it with a scanning electron beam. The transmission spectrum features two superimposed ring spectra. By evaluating only relative peak shifts between the two ring spectra the external influences are excluded. The experiment was performed in situ, inside the vacuum chamber of a scanning electron microscope, by providing it with vacuum fiber feedthroughs. The optical input and output signals are transported by single mode fibers, glued (with UV-curable glue) in a near-vertical position above grating couplers. The optical circuit, with grating couplers, waveguides, tapering sections, and ring resonators, was fabricated by DUV lithography in a standard CMOS foundry [10]. Light was generated by a super luminescent LED with center wavelength at 1530 nm and detected by a spectrum analyzer with a resolution of 60 pm.

The right ring resonator was trimmed in three subsequent steps, as depicted in Fig. 2. In a first step we have scanned a 0.21 nA beam across a 25.6 μm x 22.1 μm area for 360 s, and
measured a resonance wavelength shift of 1.24 nm. In the next step the same beam was scanned over the same area for 300 s to reach an extra shift of 0.82 nm. In a final step a 0.84 nA beam was scanned over a 17.1 μm x 14.8 μm area for 400 s, leading to a subsequent shift of 2.77 nm. In total this leads to a 4.91 nm red shift of the resonance wavelength of the silicon ring. Figure 2 shows the evolution of the resonance peaks, extracted from the measured spectra (one spectrum was measured every 10 s). In the final step the resonance wavelength of the right ring was equated with that of the left ring. To evaluate the propagation losses we have extracted the Q-factors from the measured spectra, as depicted in Fig. 2. It is clear that the Q-factor slightly decreases in the third step of the experiment, which is also visible in the right part of Fig. 2. We argue that the increase during the second step is caused by a varying coupling constant, whereas the decrease in the third step is caused by increasing propagation losses.

It is clear from the graph that in the beginning of the electron beam exposure there is a small blue shift of the resonance (between 100 pm and 200 pm blue shift in our experiments), and after the exposure there is settling of the peak towards longer wavelengths (we have measured 153 pm red shift, 45 minutes after the second step). We argue that this is partly caused by charging of the oxide and subsequent generation of carriers in the silicon, and partly by temperature variations due to electron bombardment. It was reported [24] that compacted silica tends to relax over a period of tens of hours after irradiation. However, we have measured a subsequent red shift of 0.15 nm, 5 days after the actual irradiation. This effect needs further investigation.

Fig. 2 Left: Three subsequent steps were used to equate the resonance wavelength of the right ring resonator with that of the left ring resonator. The curves represent the position of the fitted peak maxima. The grey lines (color online: red) indicate the actual electron beam irradiation; the black lines show the settling after irradiation. The dotted red line is the region where both resonance peaks merge and our single peak fitting algorithm yields inconclusive results. The Q-factors of the trimmed ring during the three steps of the experiment were extracted from the measured spectra. Right: Evolution of the resonance during the third stage of the experiment. The Q-factor of the trimmed ring decreases from $1.7 \times 10^4$ at 1526 nm to $1.0 \times 10^4$ at 1528 nm.
Fig. 3. Cross-section of the 220 nm thick silicon ring resonator. The 2 keV electrons penetrate 70 nm into silicon and oxide, and lead to volume compaction only in the oxide. This effect generates a tensile strain in the silicon, parallel to the substrate. The effect of silicon strain dominates the refractive index change. In the bottom drawing the first principal strain obtained from a finite element simulation was overlaid. (Color online: dark blue = compressive strain 0.05; red = tensile strain 0.17)

4. Discussion: cladding refractive index change versus core compaction-induced stress

Two distinct physical processes cause the effective index change of the mode in the silicon ring resonator, as depicted in Fig. 3. The first is a larger refractive index of the oxide cladding due to volume compaction; the second is the stress this oxide compaction induces in the silicon lattice. In our experiment we have worked with 2 keV electrons, which have a penetration depth of about 70 nm in Si and SiO$_2$ (this was calculated with Monte Carlo simulations, and confirmed by [24]). The silicon ring is 220 nm thick, on top of a 2 μm oxide layer; therefore the electrons can not penetrate the silicon. The mode overlap with the compacted oxide is lower than 1.5%, as was calculated with a mode expansion tool. From [24, 28] we have estimated the maximum amount of refractive index change to be below 3% (i.e. for a compaction of about 10%), with a total irradiation dose of $2.8 \times 10^{23}$ keV/cm$^3$ (the total dose in our experiment). This can not lead to more than 0.5 nm shift in resonance wavelength. We can thus conclude that the largest fraction of the observed resonance wavelength shift is caused by strain in the silicon lattice. Finite elements simulations were used to evaluate this effect (with a variable mesh size, down to 10 nm where a large stress gradient is reached), as shown in Fig. 3. The overlay picture illustrates the deformed mesh (with a scale factor of 2) and the first principal strain in the case of a 10% compacted oxide layer with a thickness of 70 nm.

It was reported [28] that the induced stress in the compacted oxide layer saturates upon high dose electron beam irradiation. However, we have not found reports of this effect for the energy and dose range considered in our experiments. Since a complete study was beyond the scope of this work, we have chosen to estimate the influence of compaction induced stress on the effective index of the supported modes without detailed simulations of the optical mode profile in the strained lattice. We have therefore calculated the average silicon strain in the dominant direction: perpendicular to the waveguide propagation direction and in the plane of the substrate surface. The resonance wavelength shift was calculated by using only the $p_{11}$ component of the silicon elasto-optical tensor. This shift is calculated for varying oxide compaction rates and for different compacted layer thicknesses. The results, displayed in Fig. 4, support the fact that tensile strain in the silicon waveguide can account for the observed resonance wavelength shift.
Although all experiments in this report were performed with an electron beam, they can in principle be repeated with UV since the penetration depth at wavelengths between 200 nm and 400 nm is sufficiently low to create compaction induced strain. Ring resonators in other semiconductor materials can be compacted in a similar way, as well as other kinds of cavities, such as photonic crystal cavities.

5. Conclusions

We report on the trimming of a silicon ring resonator by electron beam irradiation. The oxide cladding is subject to volume compaction, causing tensile strain in the silicon lattice. Both effects generate an increase in refractive index, generating a red shift in resonance wavelength. The dominant effect is the tensile strain in silicon, since we have estimated a maximum contribution of 0.5 nm due to oxide refractive index changes (due to limited confinement in the cladding). In our experiment we have measured a maximum resonance wavelength red shift of 5 nm. In spite of a decrease in Q-factor for large resonance wavelength shifts and an ageing effect to be investigated we believe that this technique compensate for variations on wafer scale and on chip scale due to optical lithography imperfections. Specifically in combination with vertical grating couplers and in live readout, this technique is suited for rapid and automatic trimming of devices before packaging and on wafer scale.

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